

# United States Patent and Trademark Office

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/934,693 08/23/2001		Keun Hee Ahn	0465-0852P-SP	8406	
2292	7590 12/08/2004	EXAMINER			
	WART KOLASCH &	MEEK, JACOB M			
PO BOX 747 FALLS CHU	RCH, VA 22040-0747	ART UNIT	PAPER NUMBER		
	,	2637			

DATE MAILED: 12/08/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary  Examiner  Jacob Meek  2637  The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply  A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).			Applicatio	n No.	Applicant(s)				
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### **DETAILED ACTION**

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### Claim Objections

1. Claim 1 is objected to because of the following informalities: the preamble of claim 1 states " ... where frequency offset and phase jitter are restored ... ." This would appear to be contradictory to the functionality indicated by the invention, which would appear to be appropriate for the removal of frequency offsets and phase jitter. Applicant's disclosure indicated that the intent of the invention is for the purpose minimization of frequency offsets and phase jitter. Appropriate correction is required.

Claim 10 is objected to because of the following informalities: Typographical error "fo" requires correction. Examiner will interpret as "of" for the purpose of examination.

Appropriate correction is required.

Claim 17 is objected to because of the following informalities: claim 17 is said to be dependent from claim 1 however, claim 1 is drawn to an apparatus. Examiner will analyze claim as being dependent on claim 11, which is a method claim. Appropriate correction is required.

## Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- Claims 1, 2, and 11 14 are rejected under 35 U.S.C. 103(a) as being unpatentable over Murakami et al (US Patent 6,667,317).

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With regard to Claim 1, Murakami teaches an apparatus for converting pass-band digital signals into baseband digital signals (see Figure 1, IN, 1, 2, x(n), y(n), and ) by demodulating sine/cosine wave (I/Q). Murakami teaches phase / frequency detection section for obtaining obtain phase errors (see figure 1, 5,13, 4a, and column 8, line 53 - column 9, line 4) between constellations of a blind decision signal and a directed decision signal (see figure 1, 4a and figure 4). Murakami is silent with respect to blind equalization. Examiner interprets CMA method to be the equivalent functionality of blind decision and DD method to be decision directed. Murakami teaches a frequency acquisition loop section (See Figure 1, x(n), 4a, y(n), 5, R1, and column 10, lines 28 – 41) extracting frequency offsets by means as illustrated by figure 17 where calculations are performed (see column 9, lines 10 - 27) and examiner interprets this operation occurring as a result of precalculated BW values (i.e., operation of CMA versus DD). Murakami teaches a loop section for phase tracking (see Figure 1, x(n), 4a, y(n), 5, R1, 13,  $\alpha$ ,  $\beta$ , Figure 2) where the carrier error quantity detection circuit is interpreted by the examiner to provide phase tracking functionality by the monitoring of the results of the carrier frequency error detection operation which accumulates results and examiner interprets this operation occurring as a result of precalculated BW values (i.e., operation of CMA versus DD). Murakami teaches a blind decision section for extracting the polarity of demodulated signal (see Figure 4, 17 and figure 3, 7a, e(n), 8) where CMA functionality is interpreted as being equivalent to blind decision functionality. Murakami teaches a directed decision section for extracting the polarity of demodulated signal (see Figure 4, 18 and figure 3, 7a, e(n), 8) where DD functionality is interpreted as being equivalent to directed decision functionality. It would have been obvious to one of ordinary skill in the art to utilize Murakami's device with CMA and DD functionality to provide an

adaptive equalizer with blind and directed decision capabilities as CMA is discussed widely as being the algorithm used for blind equalization (see NPL references).

With regard to claim 2, Murakami teaches the phase / frequency detection section operates in a blind- mode (CMA) or directed decision mode (DD) (see figure 4) based on lock detection for control of selection (see column 10, lines 3 – 16).

With regard to claim 11, the steps claimed as method are a restatement of the function of the specific components of the apparatus as claimed above and therefore would have been obvious considering the aforementioned rejection for the apparatus of claim 1.

With regard to claim 12, Murakami teaches his blind equalization extracts the polarity (see figure 2, 17) of the error for acquiring frequency offset (see column 10, lines 42 – 52).

With regard to claim 13, Murakami teaches his directed-decision equalization extracts the polarity (see figure 2, 18) of the tracking phase jitter (see column 10, lines 53 – 63) where this is interpreted to be equivalent to the tracking of phase jitter.

With regard to claim 14, the steps claimed as method are a restatement of the function of the specific components of the apparatus as claimed above and therefore would have been obvious considering the aforementioned rejection for the apparatus of claim 2.

### Allowable Subject Matter

3. Claims 3 – 10, and 15 – 19 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

### Other Cited References

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Greenberg (US Patent. 5,519,356) explicitly teaches a lock detection device for use with a decision device for providing multiple modes of operation. de Lantremange (US

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Patent 6,115,433), Koslov (US Patent 5,471,508) and Paik et al (US Patents 5,363,408 and 5,311,546) teach carrier recovery with changeable adaptation modes. Oh / Chin (New Blind Equalization Techniques Based on Constant Modulus Algorithm, IEEE, 5/95) and Choi /Hwang / Song (Adaptive Blind Equalization Coupled with Carrier Recovery for HDTV Modem, IEEE, 8/1993) references discuss use of equalization techniques that utilize Blind and Directed modes of operation to improve system performance.

#### Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jacob Meek whose telephone number is (571)272-3013. The examiner can normally be reached on 8:00 - 4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jay Patel can be reached on (571)272-2988. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). - (-/hh

**JMM** 

SUPERVISORY PATENT EXAMINER